

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application. Please cancel claims 2-3 without prejudice and amend claims 1, 4-5, 7-8 and 12-13 as follows:

LISTING OF CLAIMS:

1. (Currently Amended) An oscillator circuit for performing oscillation by positive feedback of an LC resonant circuit, wherein
said LC resonant circuit includes a parallel resonant circuit formed of an inductance-variable portion, allowing variation of an inductance by [[a]] first and second switch [[circuit]] circuits, and a capacitor element,
said inductance-variable portion has
first and second input/output terminals,
a spiral interconnection layer starting from said first input/output terminal,
a plurality of said first switch circuits having first terminals connected to
arbitrary positions on said interconnection layer, and having second terminals
commonly connected to said second input/output terminal, and
a plurality of said second switch circuits each having a first terminal connected
to said first terminal of one of said plurality of first switch circuits, and having a
second terminal connected to said first terminal of another one of said plurality of first
switch circuits, and
when one of said plurality of first switch circuits is turned on, or when one of
said plurality of first switch circuits and one of said plurality of second switch circuits
are turned on, the position on said interconnection layer connected to said first

terminal of said turned-on first switch circuit is electrically coupled to said second input/output terminal.

Claim 2. (cancelled)

Claim 3. (cancelled)

4. (Currently Amended) ~~The~~An oscillator circuit according to claim 1 for performing oscillation by positive feedback of an LC resonant circuit, wherein said LC resonant circuit includes a parallel resonant circuit formed of an inductance-variable portion, allowing variation of an inductance by a switch circuit, and a capacitor element, wherein

said inductance-variable portion includes has

first and second input/output terminals,

a plurality of spiral interconnection layers starting from said first input/output terminal, and formed on a semiconductor substrate with an interlayer insulating film therebetween, and

a plurality of ~~said plurality of~~ switch circuits connected between trailing ends of ~~said plurality of~~ interconnection layers and said second input/output terminal, respectively, and

when one of said plurality of switch circuits is turned on, the trailing end of said interconnection layer included in said plurality of interconnection layers and connected to said turned-on switch circuit is electrically coupled to said second input/output terminal.

5. (Currently Amended) The oscillator circuit according to claim 3 1, wherein
each of said first and second switch [[circuit]] circuits includes a transistor element to
be turned on/off in accordance with a voltage level of a control voltage.

6. (Original) The oscillator circuit according to claim 1, wherein said capacitor
element in said LC resonant circuit has a variable capacitance value.

7. (Currently Amended) An oscillator circuit, comprising:
a pair of transistors cross-coupled to each other; and
an LC resonant circuit of a differential type coupled to said pair of transistors
in a feedback manner; wherein
said LC resonant circuit includes
first and second inductance-variable portions including first and second
input/output terminals, said second input/output terminals being commonly
connected to a fixed node, and said first and second inductance-variable portions
being capable of varying inductances, and
a first switch circuit coupled between the first input/output terminals of said
first and second inductance-variable portions,

each of said first and second inductance-variable portions has
a spiral interconnection layer starting from said first input/output terminal and
~~formed on a semiconductor substrate with an interlayer insulating film therebetween,~~
and

a plurality of second switch circuits having first terminals connected to arbitrary positions on said interconnection layer, respectively, and having second terminals commonly connected to said second input/output terminal, respectively,

when one of said plurality of second switch circuits is turned on, the position on said interconnection layer connected to said turned-on second switch circuit is electrically coupled to said second input/output terminal, and

when said first switch circuit is turned on in response to the turn-on of said second switch circuit, said first switch circuit electrically couples said first and second inductance-variable portions.

8. (Currently Amended) An oscillator circuit, comprising:

a pair of transistors cross-coupled to each other; and

an LC resonant circuit of a differential type coupled to said pair of transistors in a feedback manner; wherein

said LC resonant circuit includes

first and second inductance-variable portions including first and second input/output terminals, said second input/output terminals being commonly connected to a fixed node, and said first and second inductance-variable portions being capable of varying inductances, and

a first switch circuit coupled between the first input/output terminals of said first and second inductance-variable portions,

each of said first and second inductance-variable portions has

a plurality of spiral interconnection layers starting from said first input/output terminal ~~and formed on a semiconductor substrate with an interlayer insulating film therebetween, and~~

a plurality of second switch circuits coupled between trailing ends of said plurality of interconnection layers and said second input/output terminal, respectively,

when one of said plurality of second switch circuits is turned on, the trailing end of said interconnection layer included in said plurality of interconnection layers and connected to said turned-on second switch circuit is electrically coupled to said second input/output terminal, and

when said first switch circuit is turned on in response to the turn-on of said second switch circuit, said first switch circuit electrically couples said first and second inductance-variable portions.

9. (Original) The oscillator circuit according to claim 7, wherein said first and second inductance-variable portions form a differential inductor element.

10. (Original) The oscillator circuit according to claim 7, wherein each of said first and second switch circuits includes a transistor element to be turned on/off in accordance with a voltage level of a control voltage.

11. (Original) The oscillator circuit according to claim 7, wherein said capacitor element in said LC resonant circuit has a variable capacitance value.

12. (Currently Amended) An L load differential circuit, comprising an inductor pair including first and second inductance-variable portions having second input/output terminals commonly connected to a fixed node and being capable of varying inductances, and a first switch circuit coupled between first input/output terminals of said first and second inductance-variable portions, wherein

each of said first and second inductance-variable portions has

a spiral interconnection layer starting from said first input/output terminal and formed on a semiconductor substrate with an interlayer insulating film therebetween, and

a plurality of second switch circuits having first terminals connected to arbitrary positions on said interconnection layer, respectively, and having second terminals commonly connected to said second input/output terminal, respectively,

when one of said plurality of second switch circuits is turned on, the position on said interconnection layer connected to said turned-on second switch circuit is electrically coupled to said second input/output terminal, and

when said first switch circuit is turned on in response to the turn-on of said second switch circuit, said first switch circuit electrically couples said first and second inductance-variable portions.

13. (Currently Amended) An L load differential circuit, comprising an inductor pair including first and second inductance-variable portions having second input/output terminals commonly connected to a fixed node and being capable of varying inductances, and a first switch circuit coupled between first input/output terminals of said first and second inductance-variable portions, wherein

each of said first and second inductance-variable portions has
a plurality of spiral interconnection layers starting from said first input/output
terminal and formed on a semiconductor substrate with an interlayer insulating film
therebetween, and

a plurality of second switch circuits coupled between trailing ends of said
plurality of interconnection layers and said second input/output terminal, respectively,
when one of said plurality of second switch circuits is turned on, the trailing
end of said interconnection layer included in said plurality of interconnection layers
and connected to said turned-on second switch circuit is electrically coupled to said
second input/output terminal, and

when said first switch circuit is turned on in response to said the turn-on of
said second switch circuit, said first switch circuit electrically couples said first and
second inductance-variable portions.